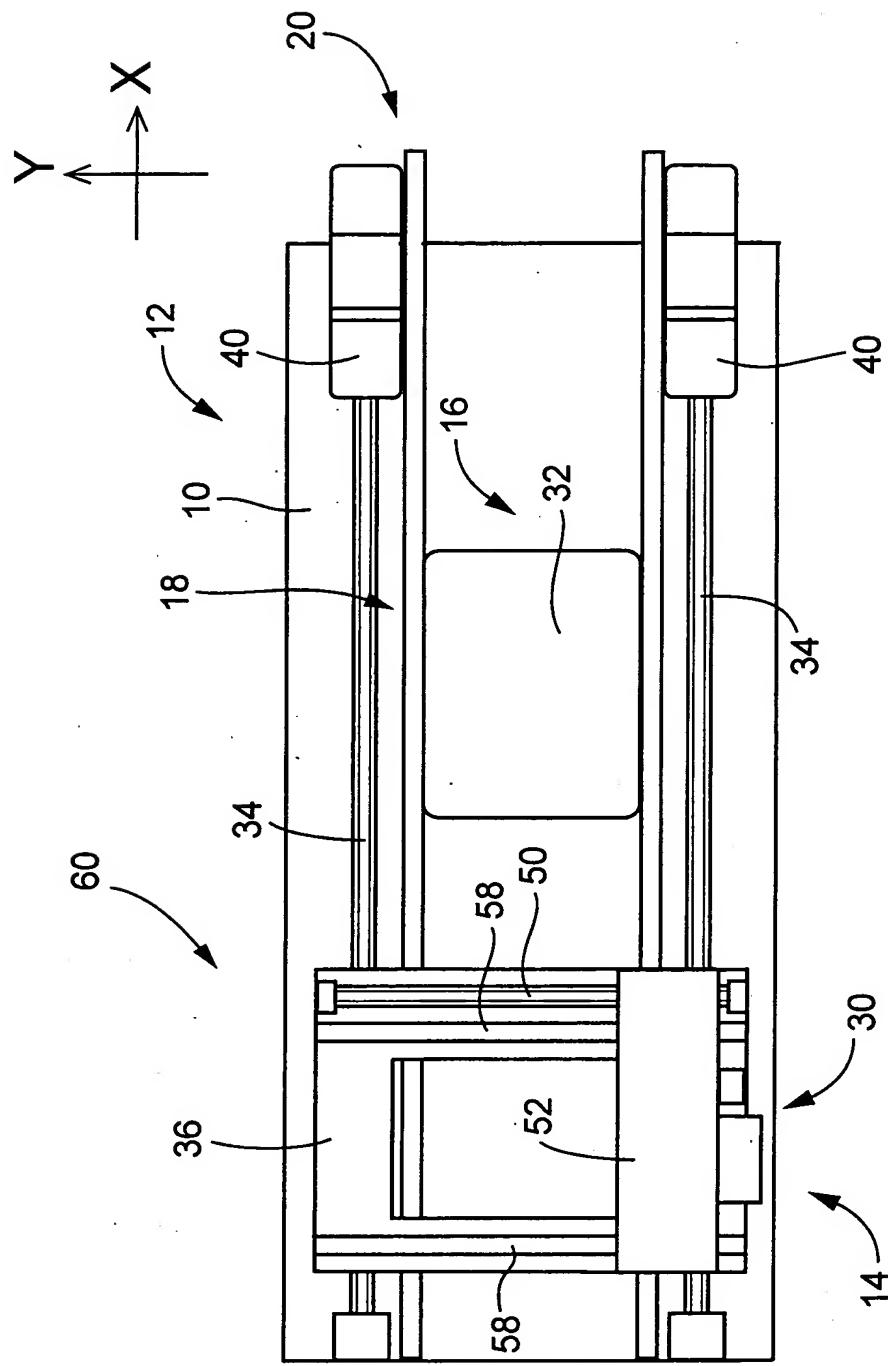


FIG. 1



10000000000000000000000000000000

FIG. 2

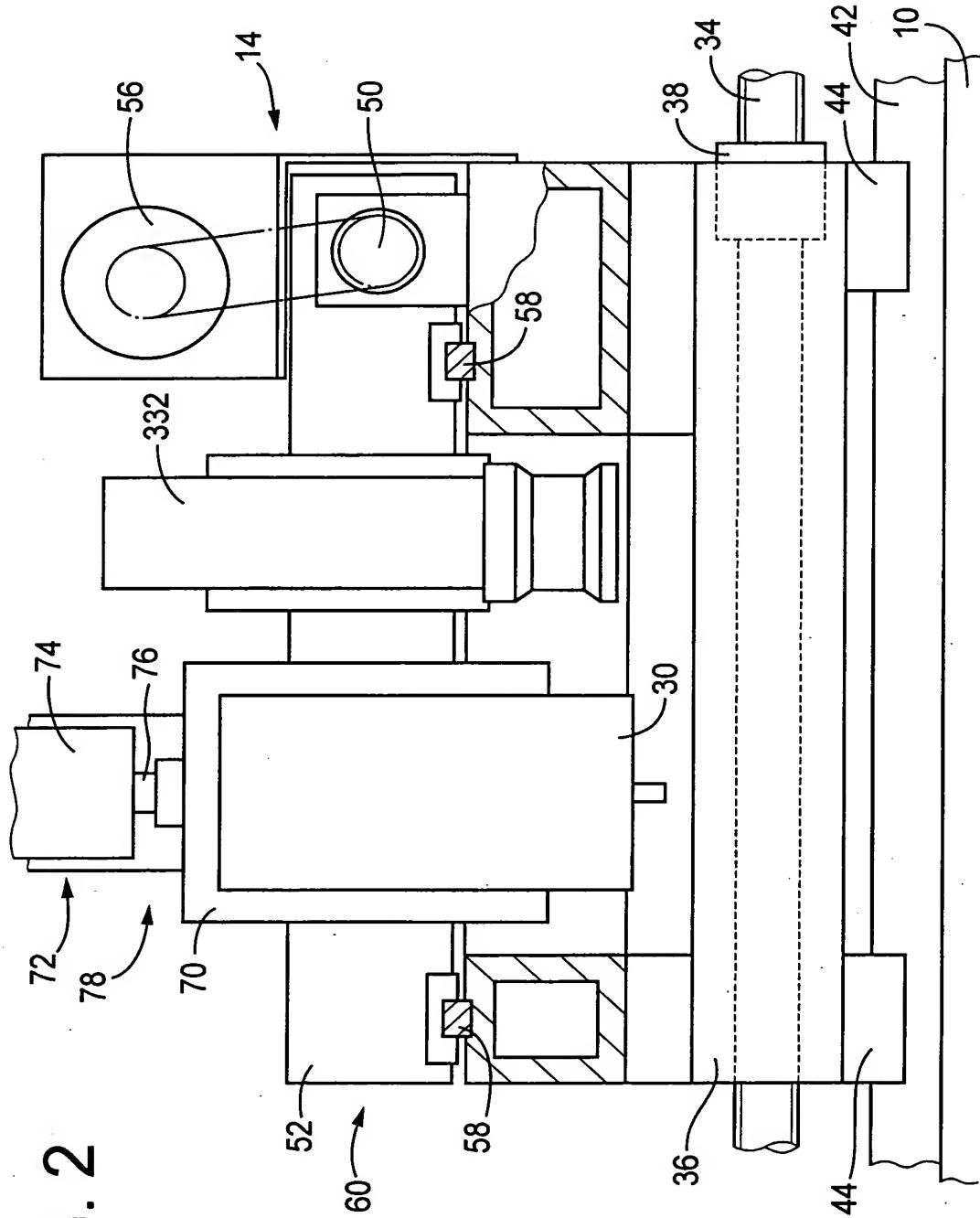
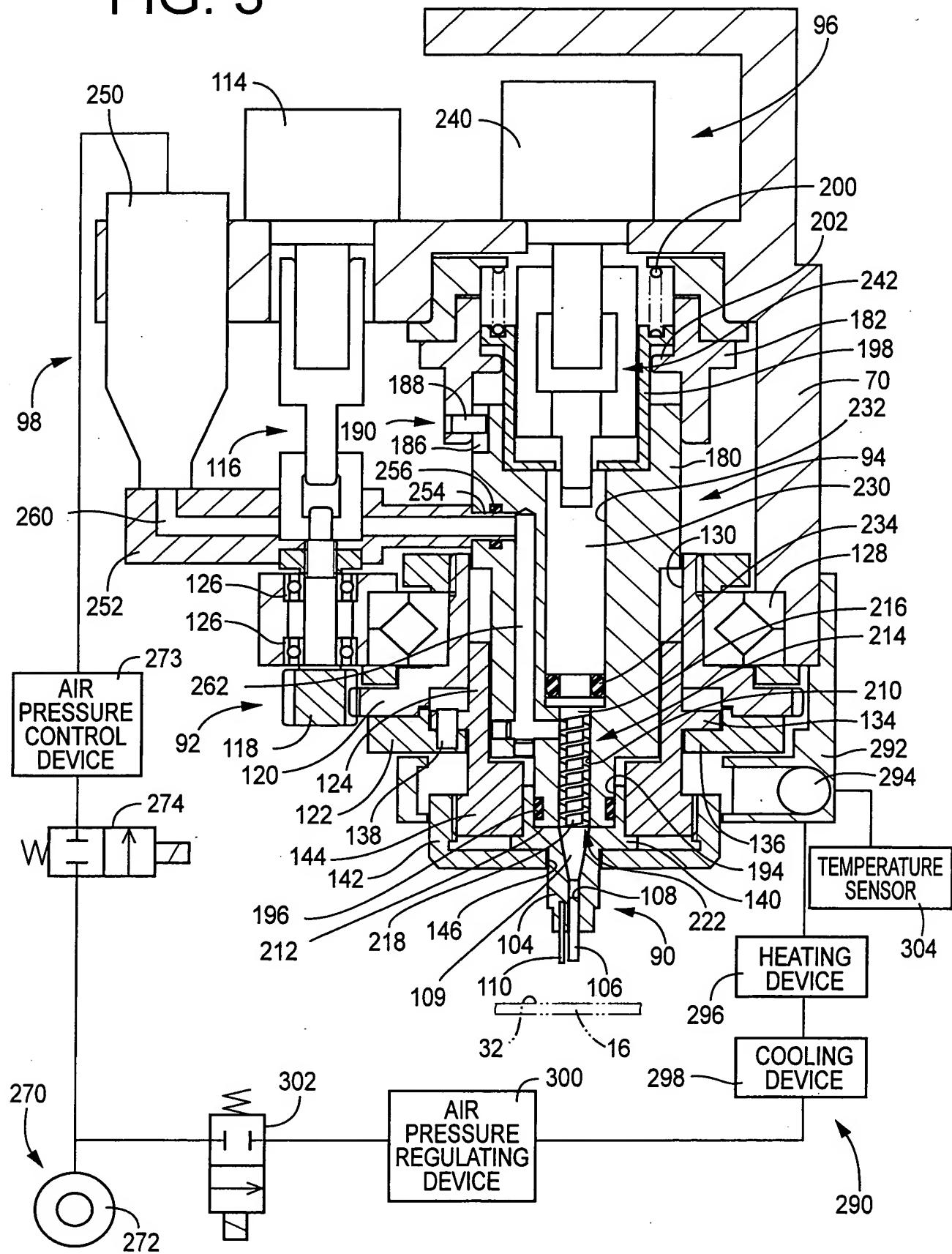


FIG. 3



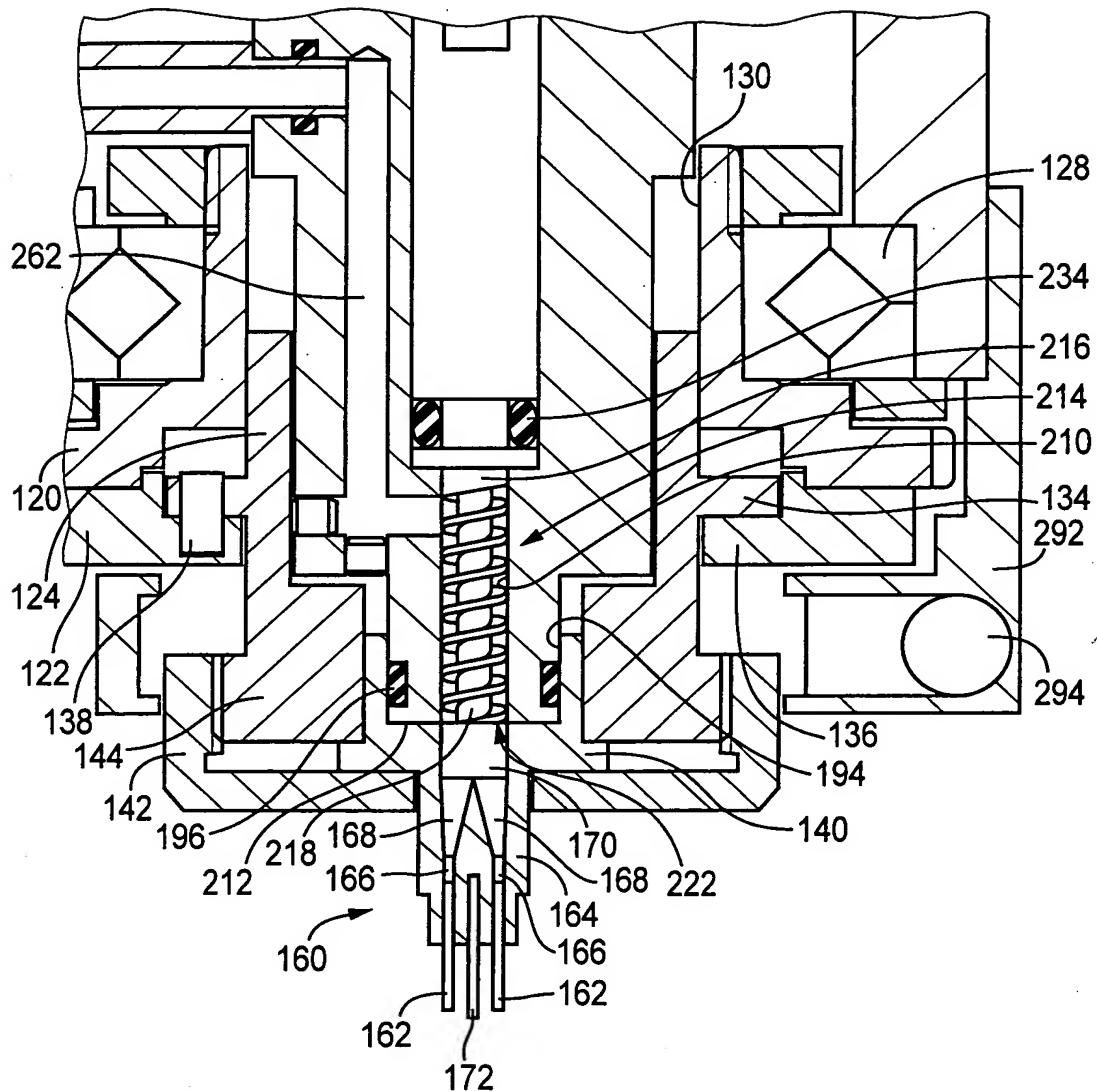


FIG. 4

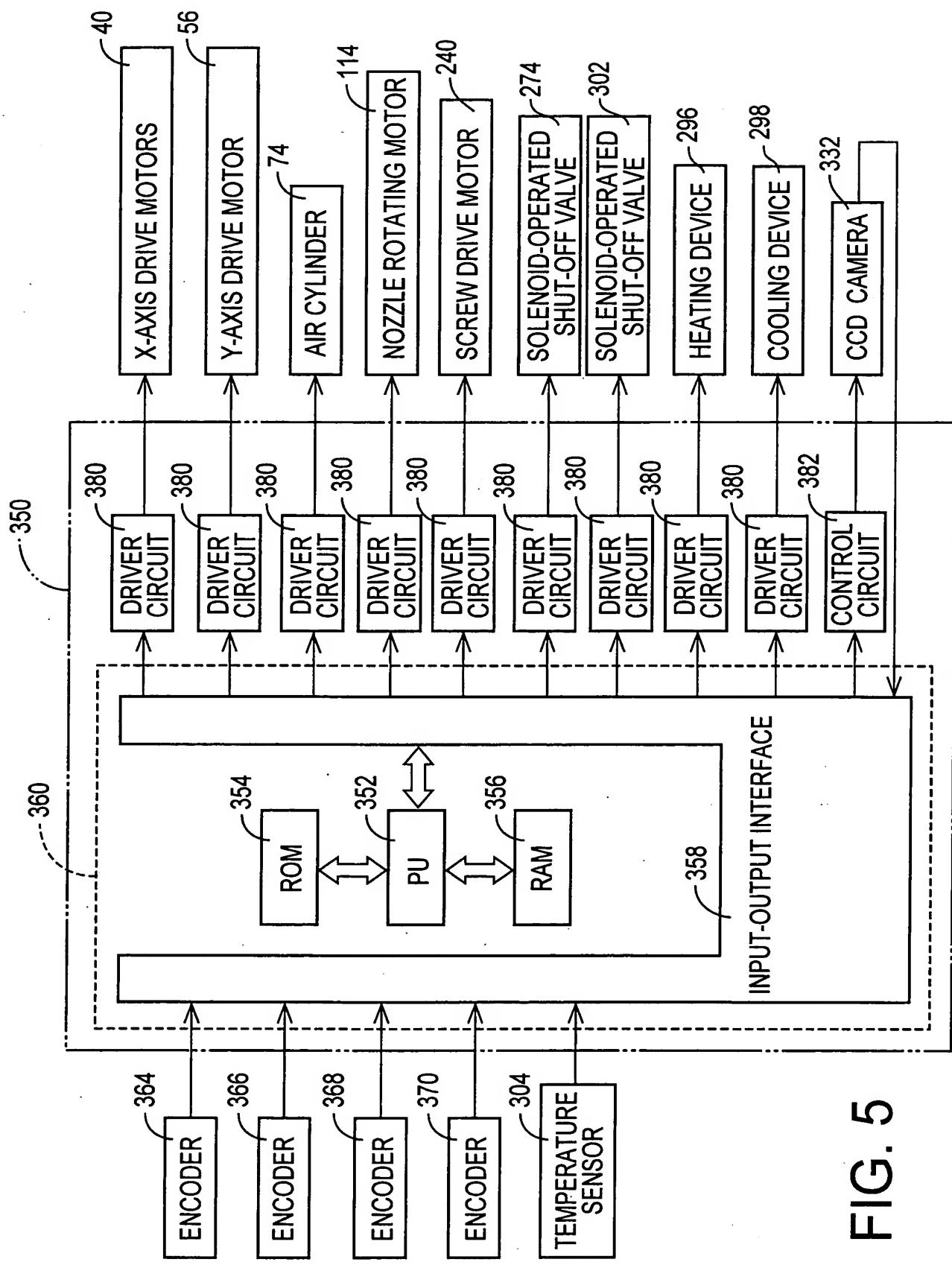


FIG. 5

FIG. 6

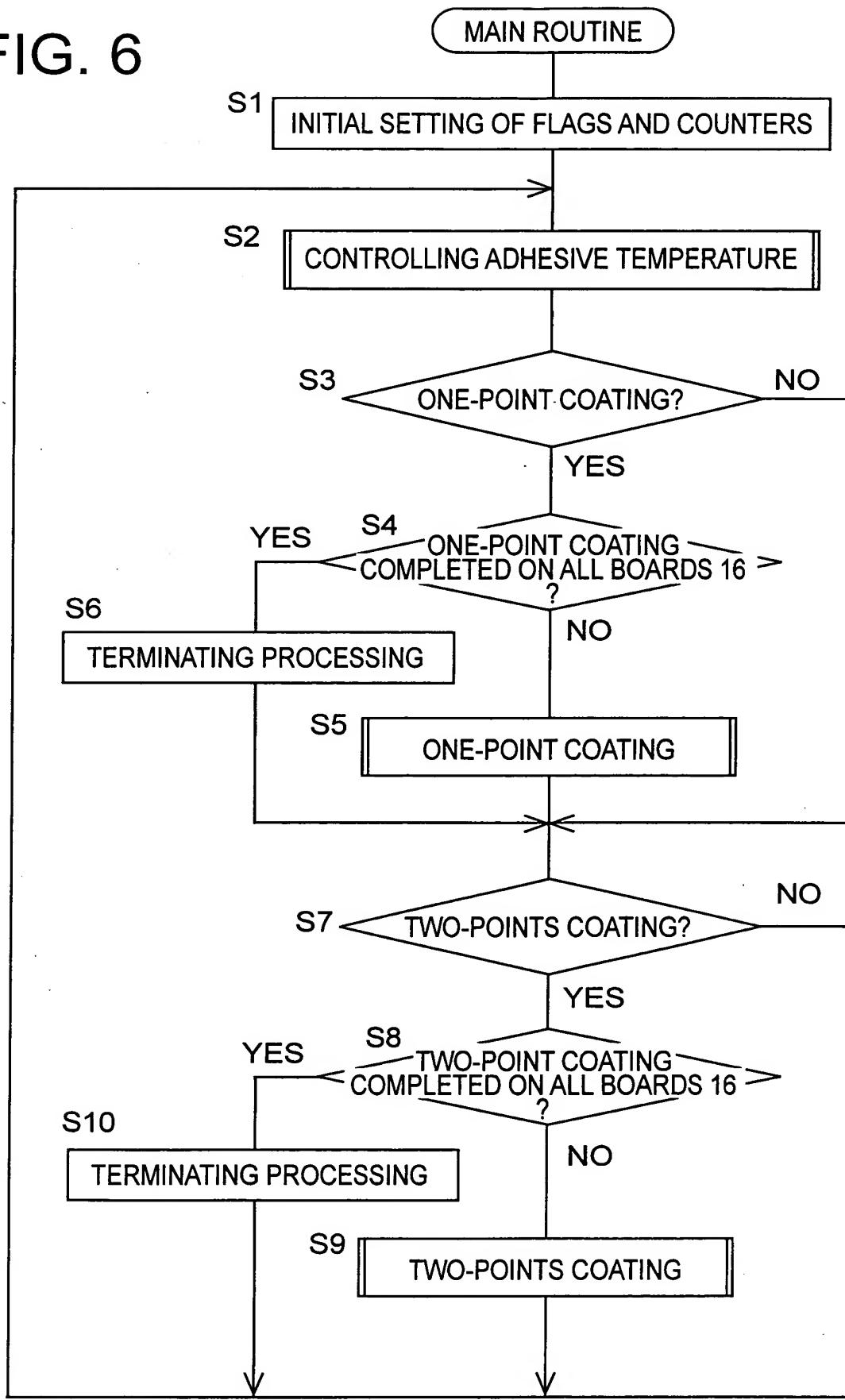


FIG. 7

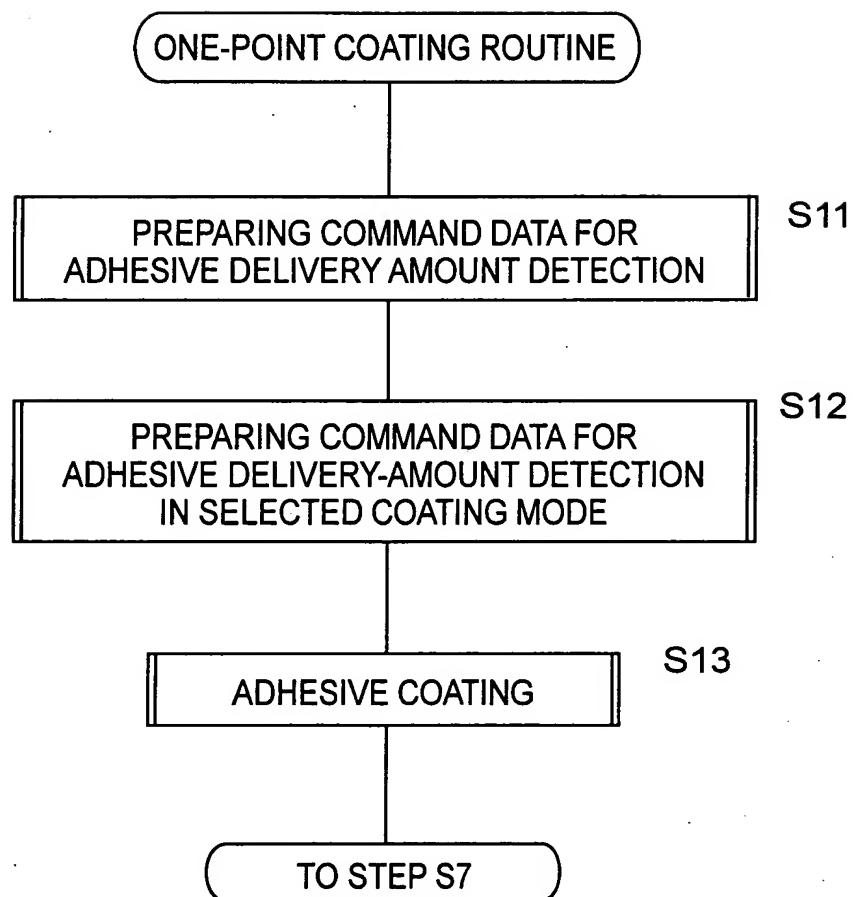
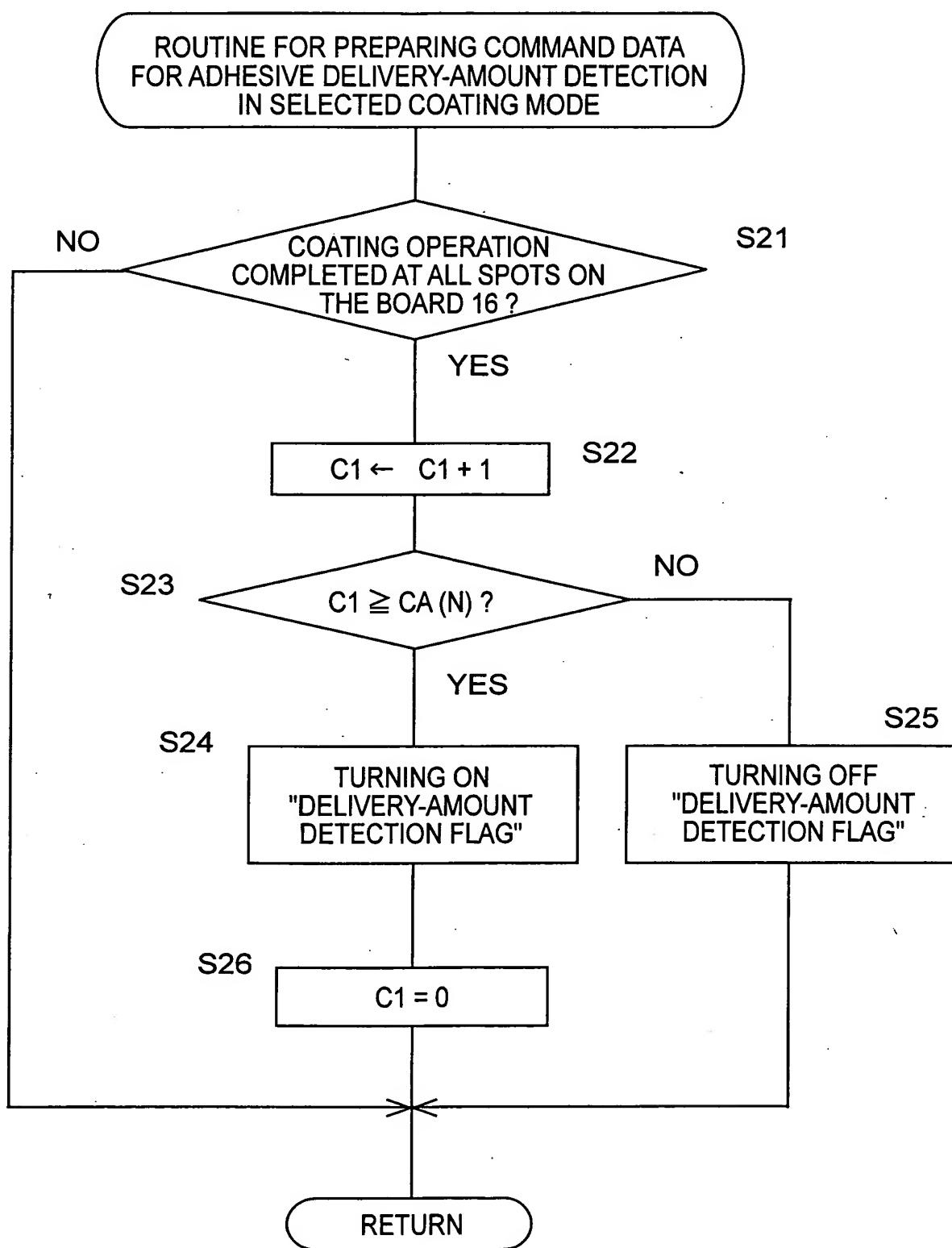


FIG. 8



4.000063300 * 4.2310003

FIG. 9

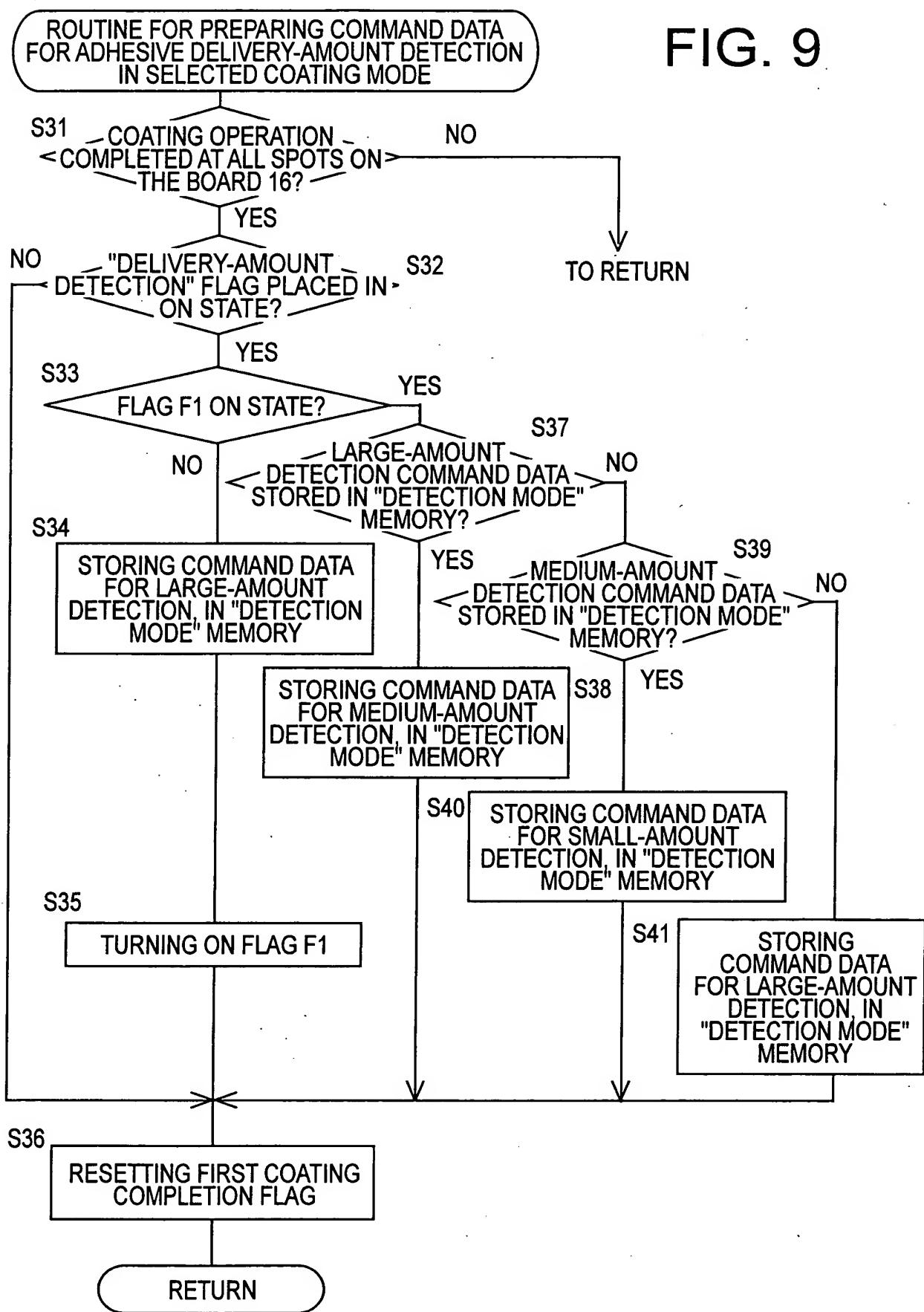
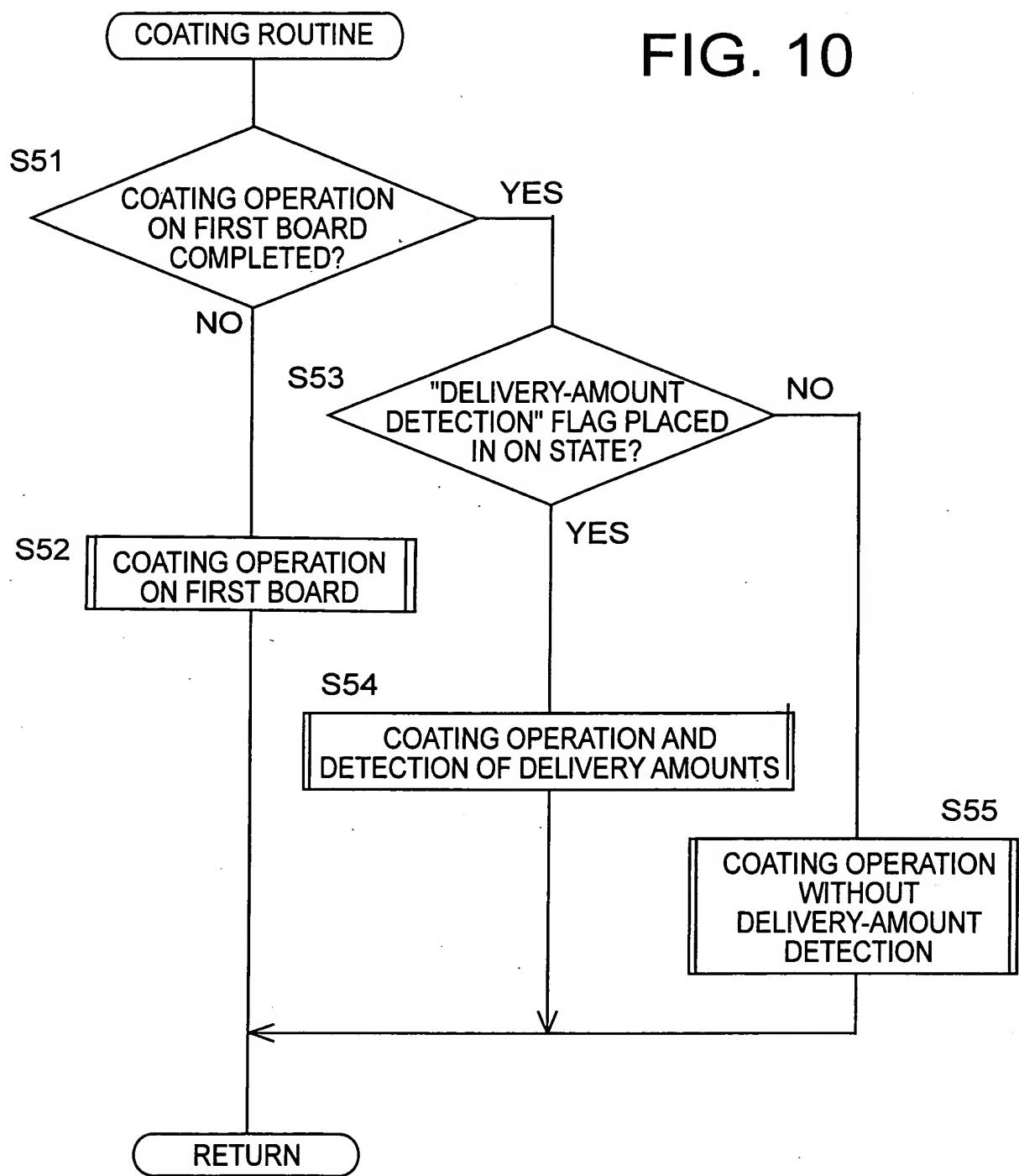
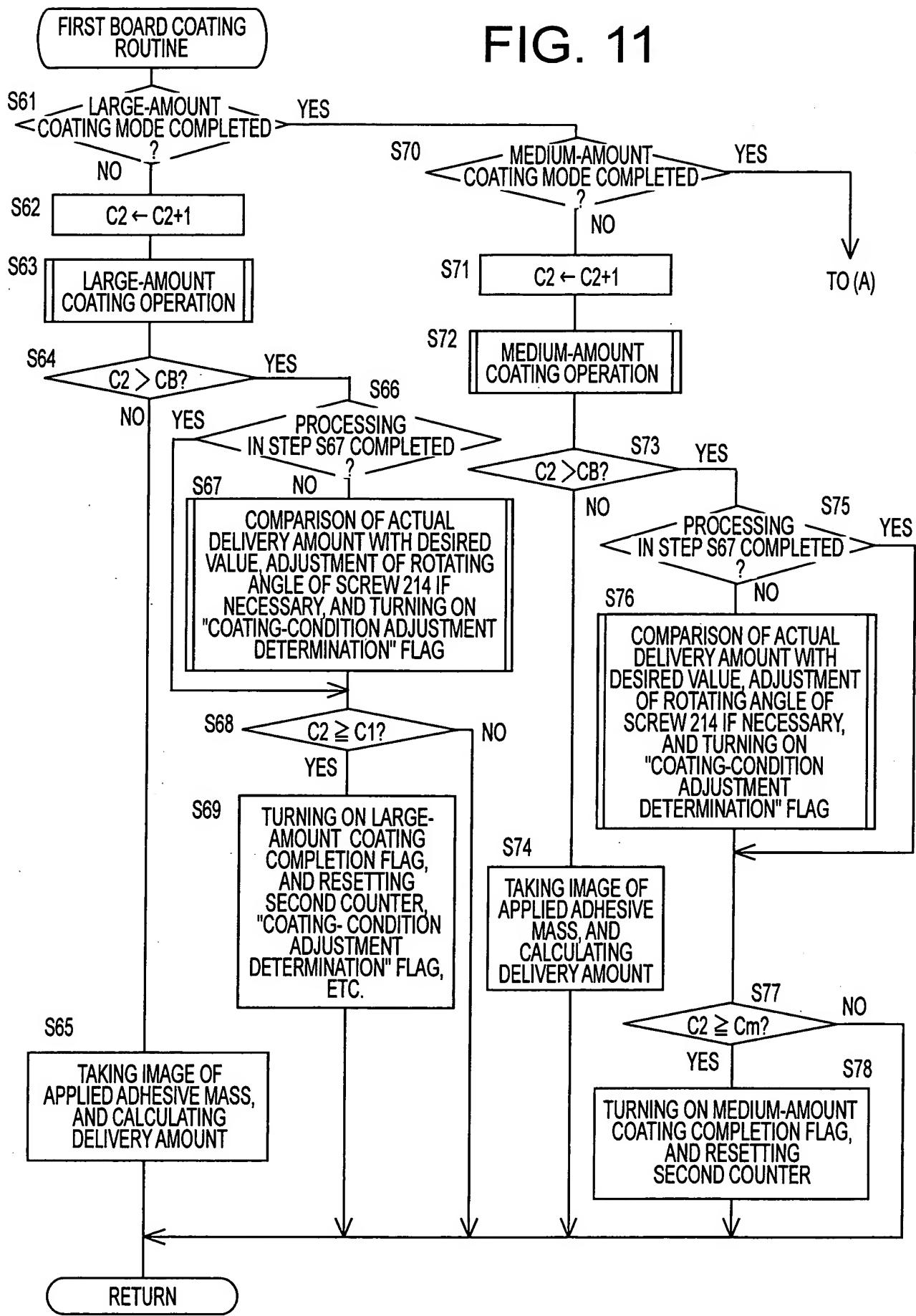


FIG. 10



T000163814 - D000638001

FIG. 11



10006380 - 1991001

FIG. 12

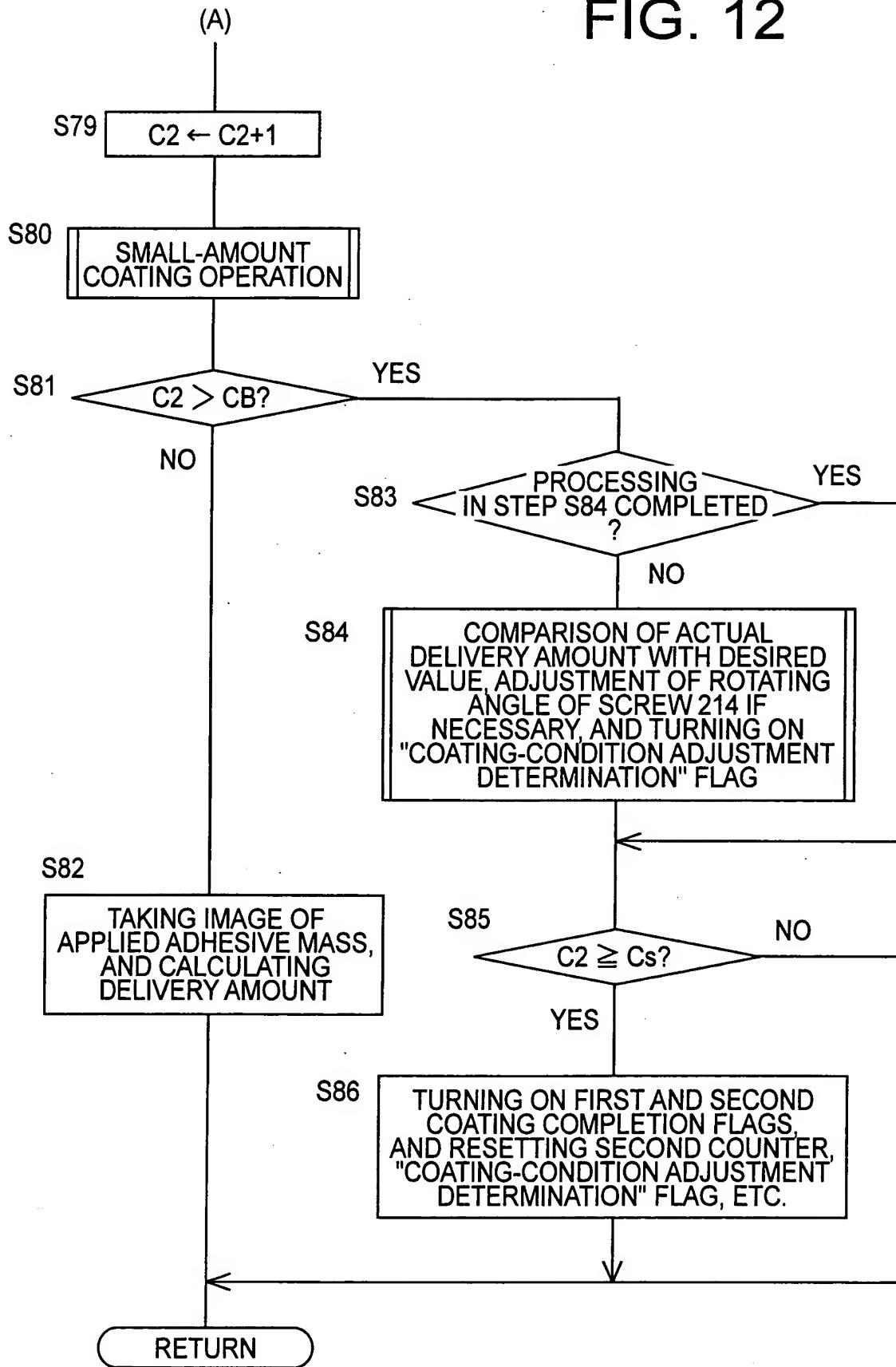


FIG. 13

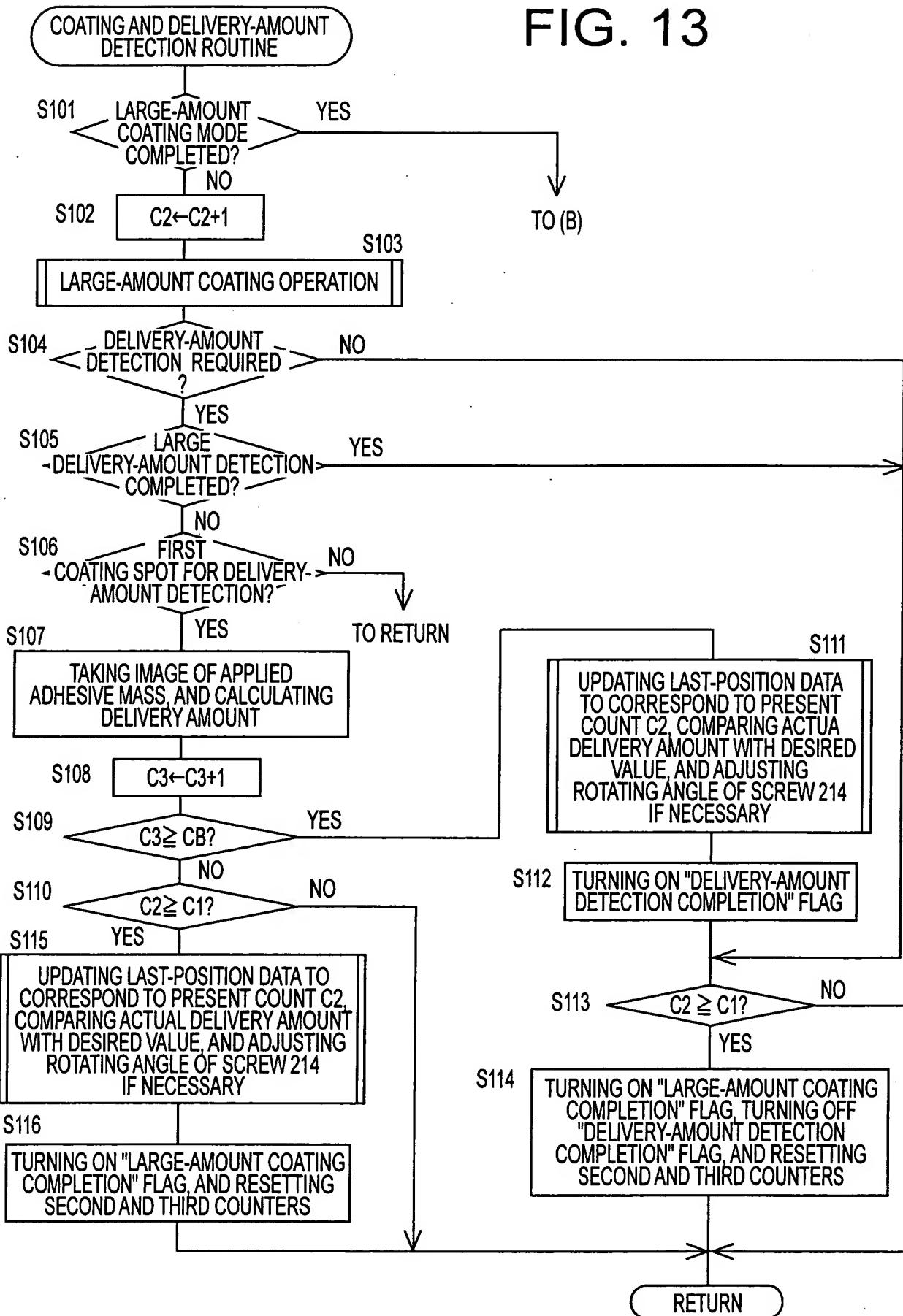
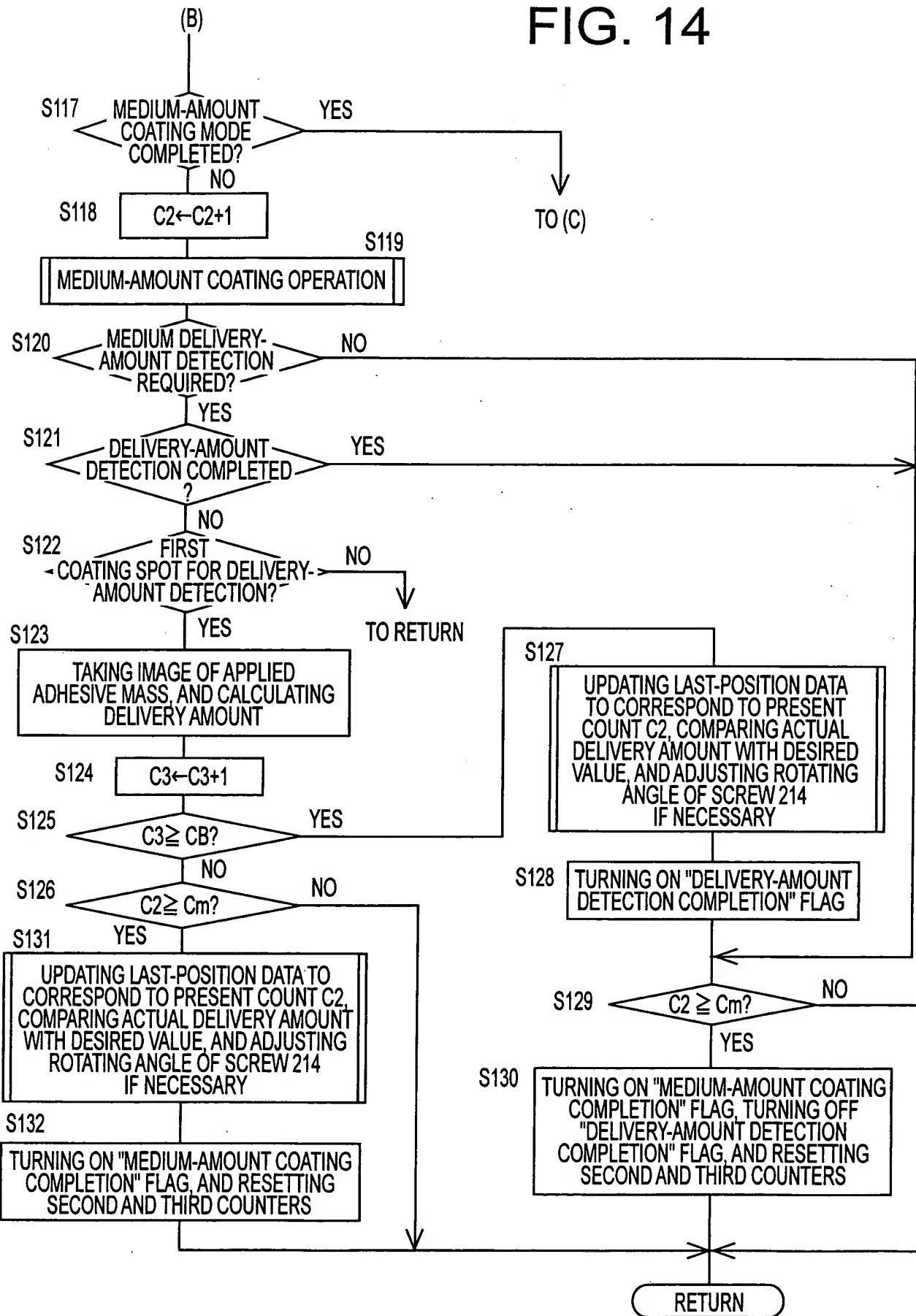


FIG. 14



TOP SECRET - DEFENSE EDITION

FIG. 15

TOP SECRET//DESIGN//C

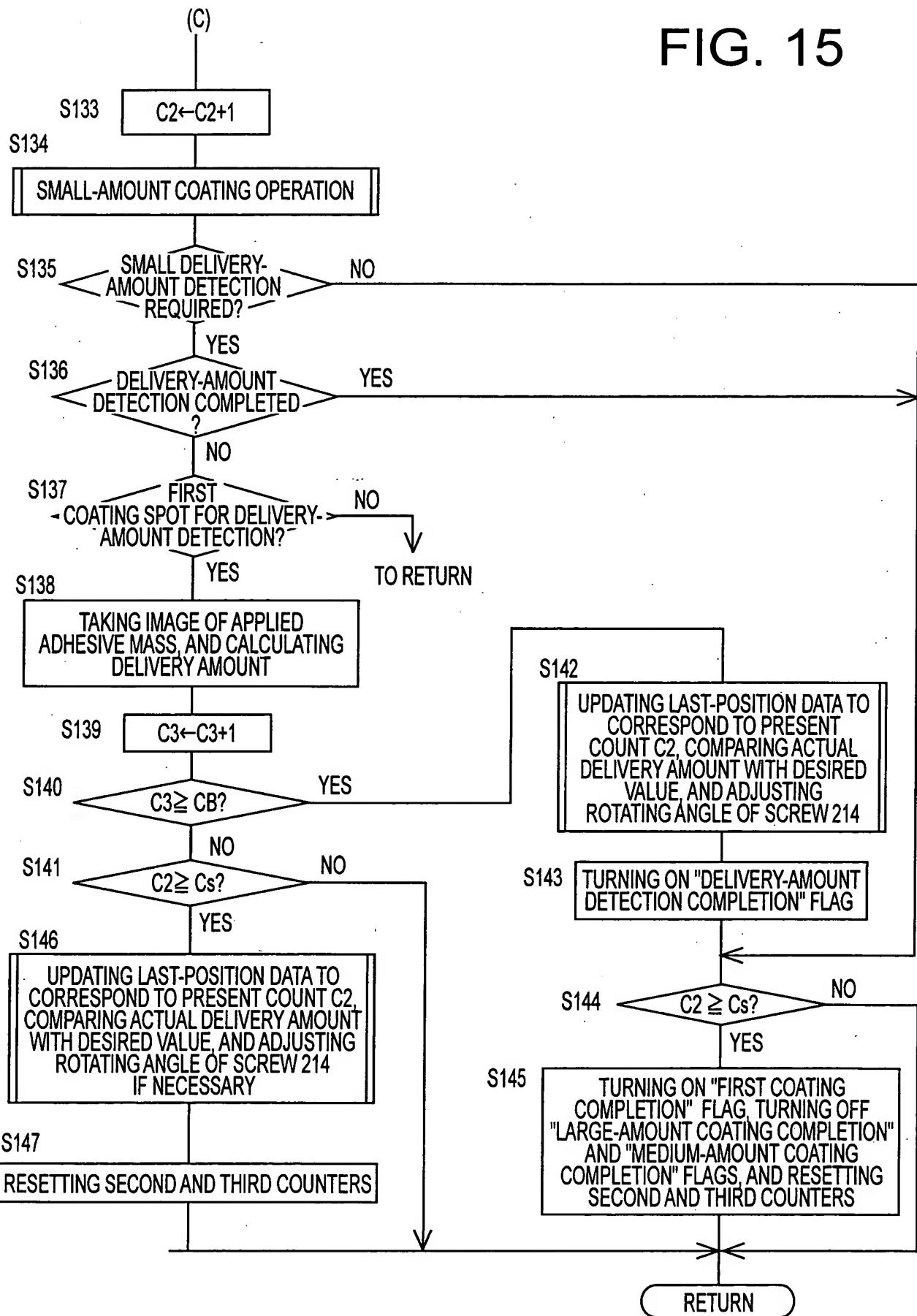


FIG. 16

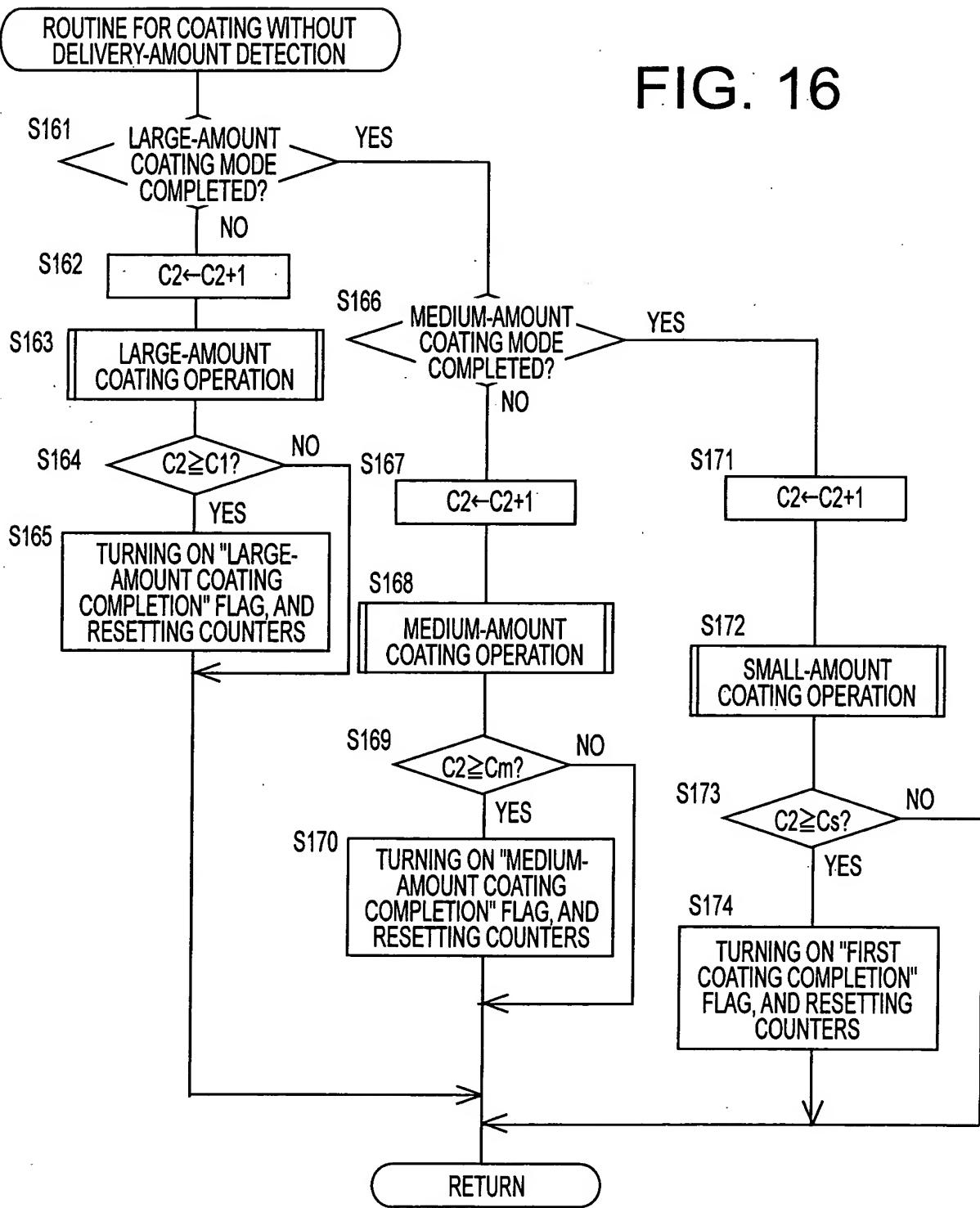
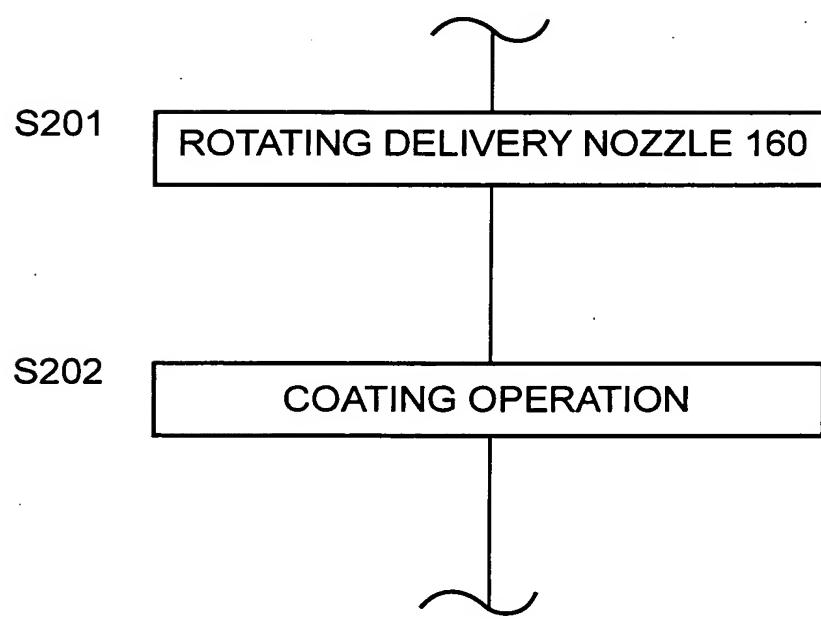


FIG. 17



1006380 • 421004

FIG. 18

356

RAM	
DELIVERY-AMOUNT DETECTION MODE MEMORY	CONTROL PROGRAM MEMORY
LARGE DELIVERY-AMOUNT MEMORY	COATING POINT-NUMBER MEMORY
MEDIUM DELIVERY-AMOUNT MEMORY	DELIVERY AMOUNT MEMORY
SMALL DELIVERY-AMOUNT MEMORY	COATING CONDITION ADJUSTMENT DETERMINATION FLAG
LAST LARGE-AMOUNT DETECTING POSITION MEMORY	DELIVERY-AMOUNT DETECTION COMPLETION FLAG
LAST MEDIUM-AMOUNT DETECTING POSITION MEMORY	FLAG F1
LAST SMALL-AMOUNT DETECTING POSITION MEMORY	FIRST COUNTER
FIRST COATING COMPLETION FLAG	SECOND COUNTER
SECOND COATING COMPLETION FLAG	THIRD COUNTER
DELIVERY-AMOUNT DETECTION FLAG	
LARGE-AMOUNT COATING COMPLETION FLAG	
MEDIUM-AMOUNT COATING COMPLETION FLAG	

FIG. 19

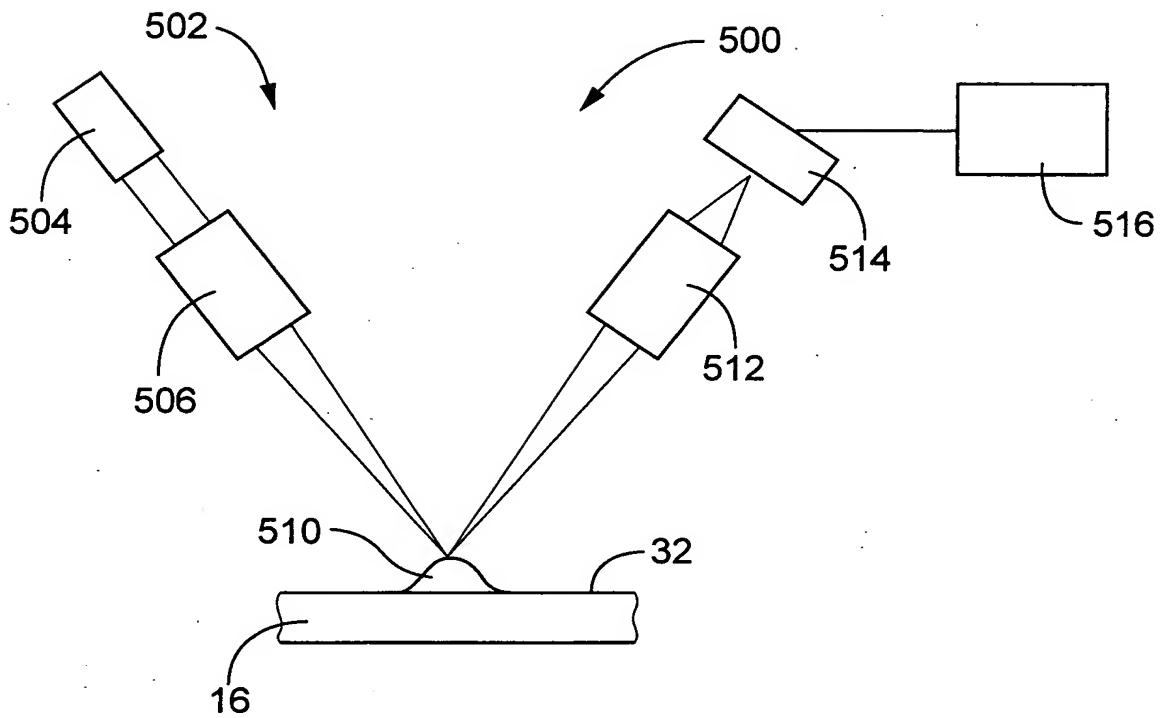


FIG. 20

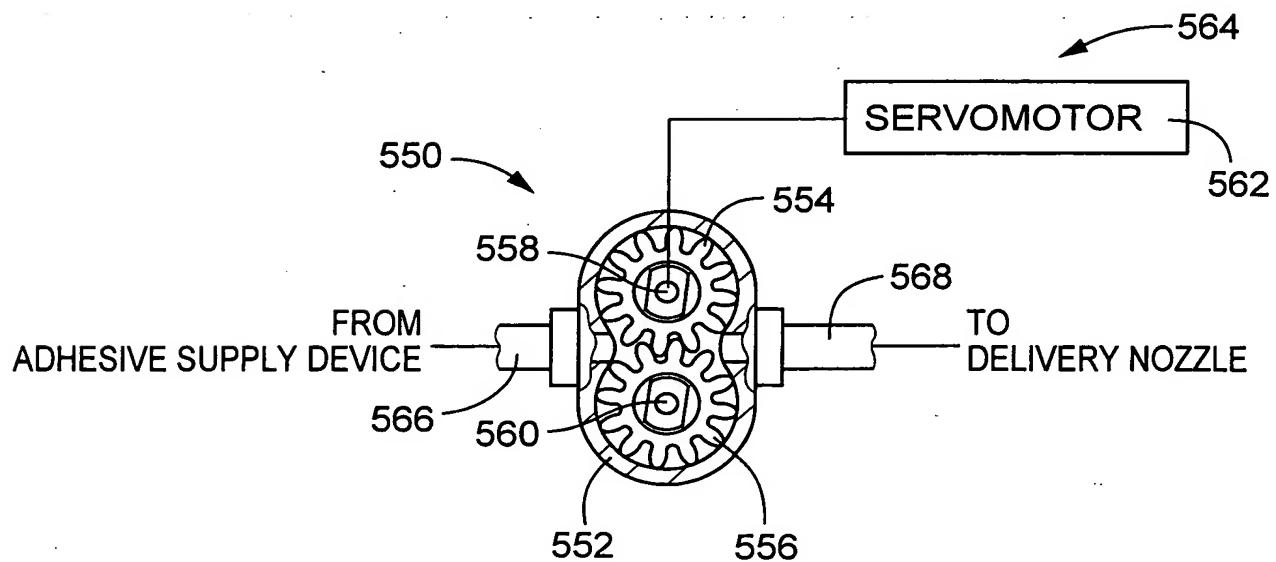
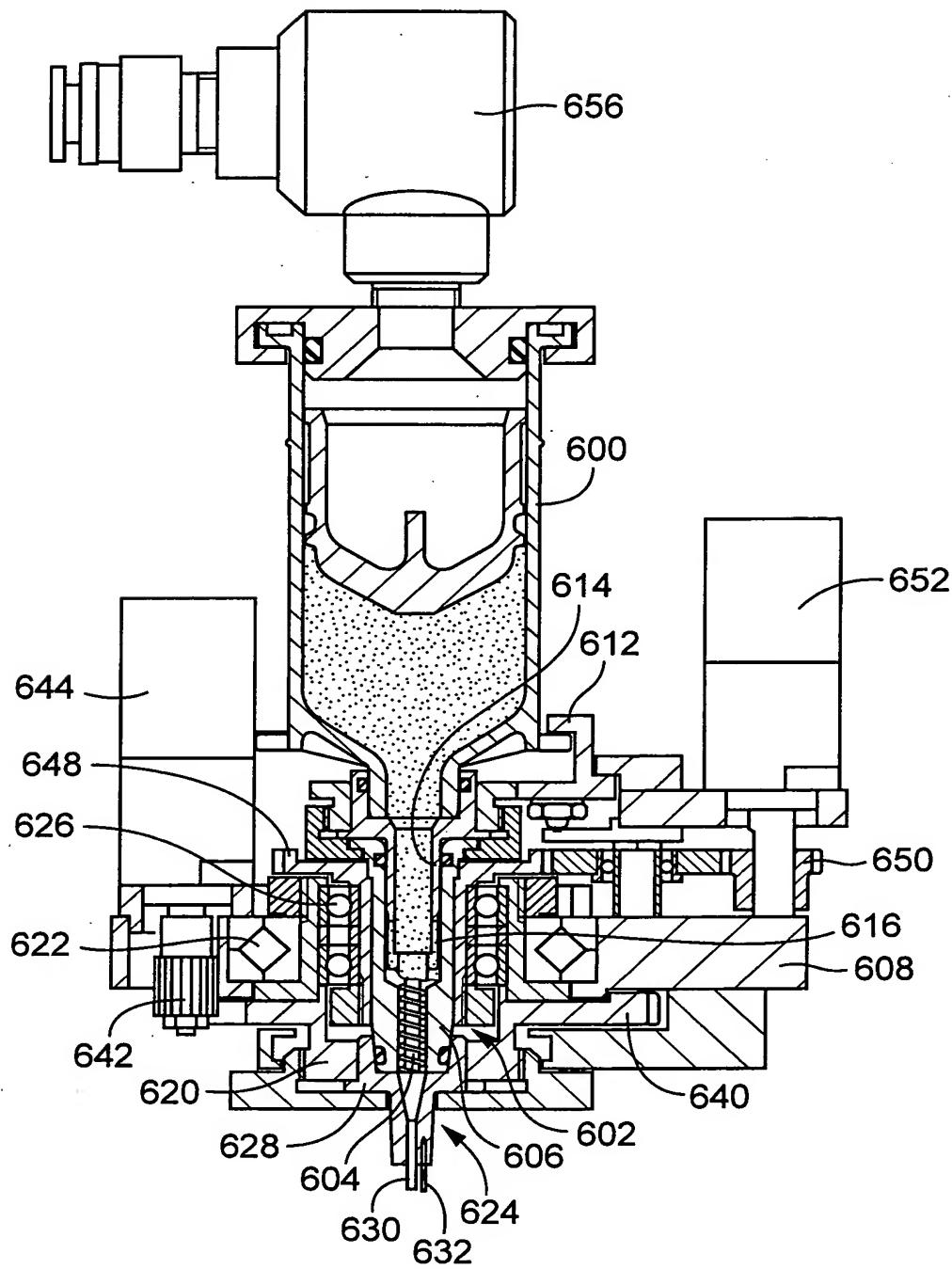


FIG. 21



4,006,380 - 12/10/01